



- (51) International Patent Classification:
H01L 23/12 (2006.01) H01L 23/48 (2006.01)
- (21) International Application Number:
PCT/US2008/084766
- (22) International Filing Date:
26 November 2008 (26.11.2008)
- (25) Filing Language:
English
- (26) Publication Language:
English
- (30) Priority Data:
P384063 17 December 2007 (17.12.2007) PL
12/195,441 21 August 2008 (21.08.2008) US

MG, MK, MN, MW, MX, MY, MZ, NA, NG, NI, NO, NZ, OM, PG, PH, PL, PT, RO, RS, RU, SC, SD, SE, SG, SK, SL, SM, ST, SV, SY, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, ZA, ZM, ZW.

- (84) Designated States (unless otherwise indicated, for every kind of regional protection available): ARIPO (BW, GH, GM, KE, LS, MW, MZ, NA, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HR, HU, IE, IS, IT, LT, LU, LV, MC, MT, NL, NO, PL, PT, RO, SE, SI, SK, TR), OAPI (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

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Declarations under Rule 4.17:

— of inventorship (Rule 4.17(iv))

Published:

- with international search report (Art. 21(3))
- before the expiration of the time limit for amending the claims and to be republished in the event of receipt of amendments (Rule 48.2(h))

- (88) Date of publication of the international search report:
13 August 2009

- (81) Designated States (unless otherwise indicated, for every kind of national protection available): AE, AG, AL, AM, AO, AT, AU, AZ, BA, BB, BG, BH, BR, BW, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DO, DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, GT, HN, HR, HU, ID, IL, IN, IS, JP, KE, KG, KM, KN, KP, KR, KZ, LA, LC, LK, LR, LS, LT, LU, LY, MA, MD, ME,

(54) Title: PLACEMENT OF AN INTEGRATED CIRCUIT

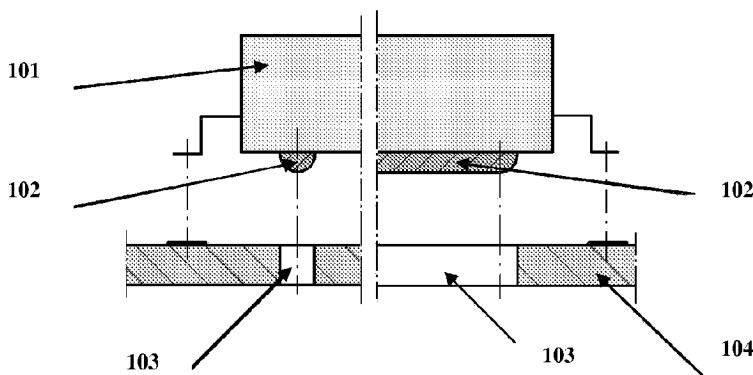


FIG. 1

(57) Abstract: Disclosed herein is a method of positioning and placing an integrated circuit on a printed circuit board. The integrated circuit comprises first geometrical elements. The first geometrical elements are of one or more predefined shapes and are located on one or more predefined surfaces of the integrated circuit. The printed circuit board comprises second geometrical elements. The second geometrical elements are shaped to accommodate the first geometrical elements. The first geometrical elements are designed to fit into the second geometrical elements. The first geometrical elements are positioned and placed over the second geometrical elements. The first geometrical elements come in contact with the second geometrical elements at two or more points. The positioning and placement of the first geometrical elements over the second geometrical elements limits displacement of connections of the integrated circuit from the printed circuit board.

WO 2009/079179 A3

A. CLASSIFICATION OF SUBJECT MATTER**H01L 23/12(2006.01)i, H01L 23/48(2006.01)i**

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC8 H01L

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Korean Utility models and applications for Utility models since 1975
Japanese Utility models and applications for Utility models since 1975

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

eKIPASS (KIPO internal) and keywords "geometrical, elements, IC, surface, second, PCB, place, and similar terms"

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	JP 58121654 A (SEIKO EPSON CORP.) 20 July 1983 See abstract, Figures 1-6 and claims 1-3	1-11
A	JP 62018722 A (TOSHIBA CORP.) 27 January 1987 See abstract, Figures 1-5 and claims 1-2	1-11
A	US 20060118971 A1 (Gracias) 8 June 2006 See abstract, Figures 1-10 and claims 1-13	1-11
A	US 20010016435 A1 (Fusimura) 23 August 2001 See abstract, Figures 1-8 and claims 1-12	1-11

 Further documents are listed in the continuation of Box C. See patent family annex.

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"&" document member of the same patent family

Date of the actual completion of the international search

22 JUNE 2009 (22.06.2009)

Date of mailing of the international search report

23 JUNE 2009 (23.06.2009)

Name and mailing address of the ISA/KR

Korean Intellectual Property Office
Government Complex-Daejeon, 139 Seonsa-ro, Seo-gu, Daejeon 302-701, Republic of Korea

Facsimile No. 82-42-472-7140

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KIM, Jun Hak

Telephone No. 82-42-481-5785



INTERNATIONAL SEARCH REPORT

Information on patent family members

International application No.

PCT/US2008/084766

Patent document cited in search report	Publication date	Patent family member(s)	Publication date
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