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(54) **FERROMAGNETIC-SEMICONDUCTOR SPIN
POLARIZER OF ELECTRONS IN
NONMAGNETIC SEMICONDUCTORS**

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(57) **ABSTRACT**

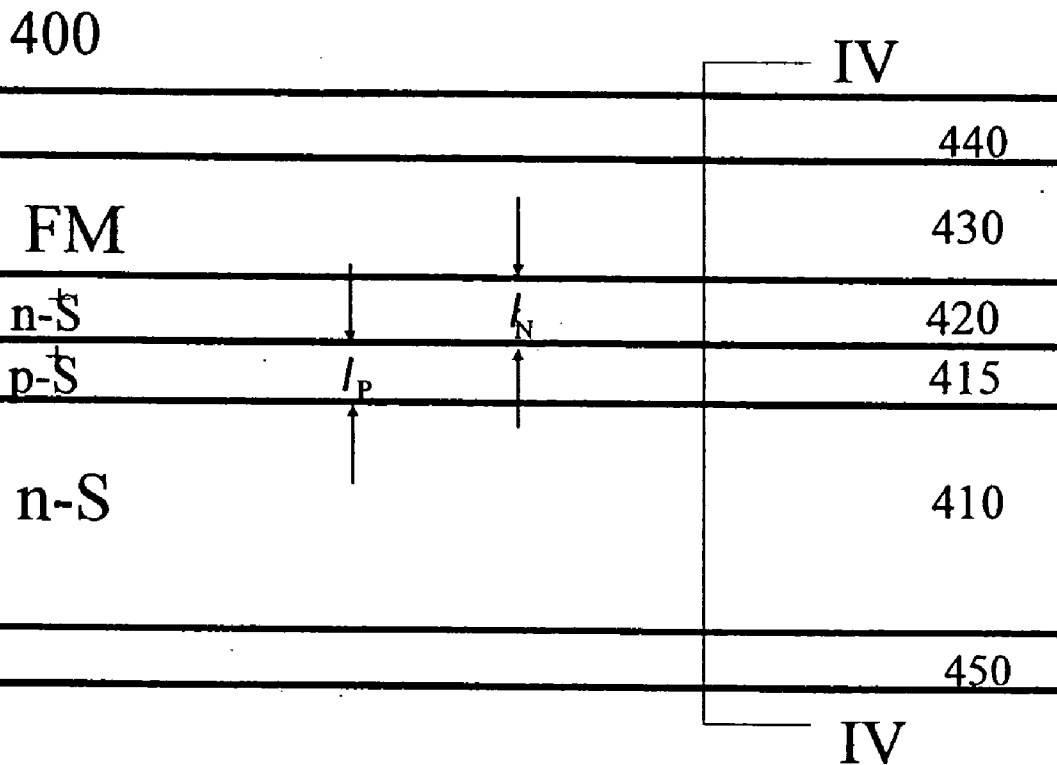
An efficient spin polarizer in nonmagnetic semiconductors is provided. Previous spin injection devices suffered from very low efficiency (less than 35%) into semiconductors. An efficient spin polarizer is provided which is based on ferromagnetic-semiconductor heterostructures and ensures spin polarization of electrons in nonmagnetic semiconductors close to 100% near the ferromagnetic-semiconductor junctions at wide temperature intervals ranging from very low temperatures to room temperatures even in the case when spin polarization of electrons in the ferromagnetic layer is relatively low.

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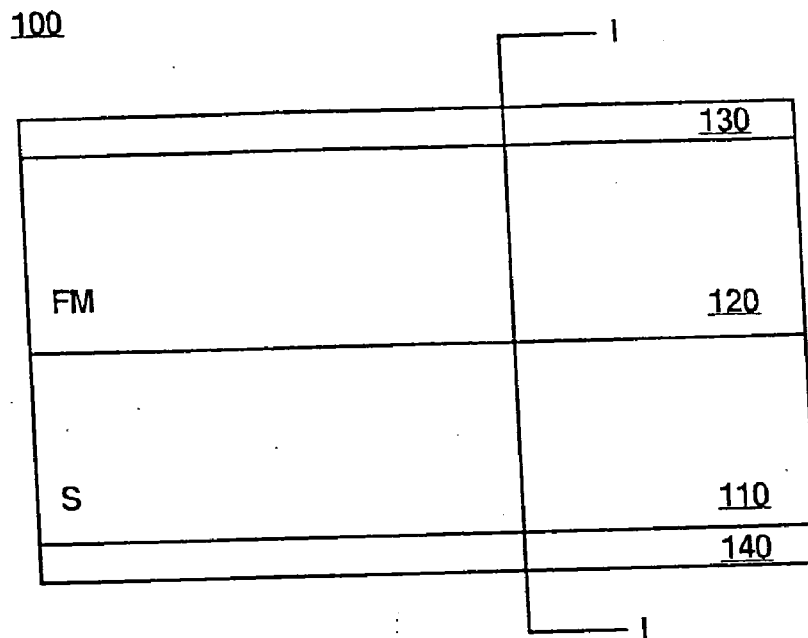


FIG. 1A

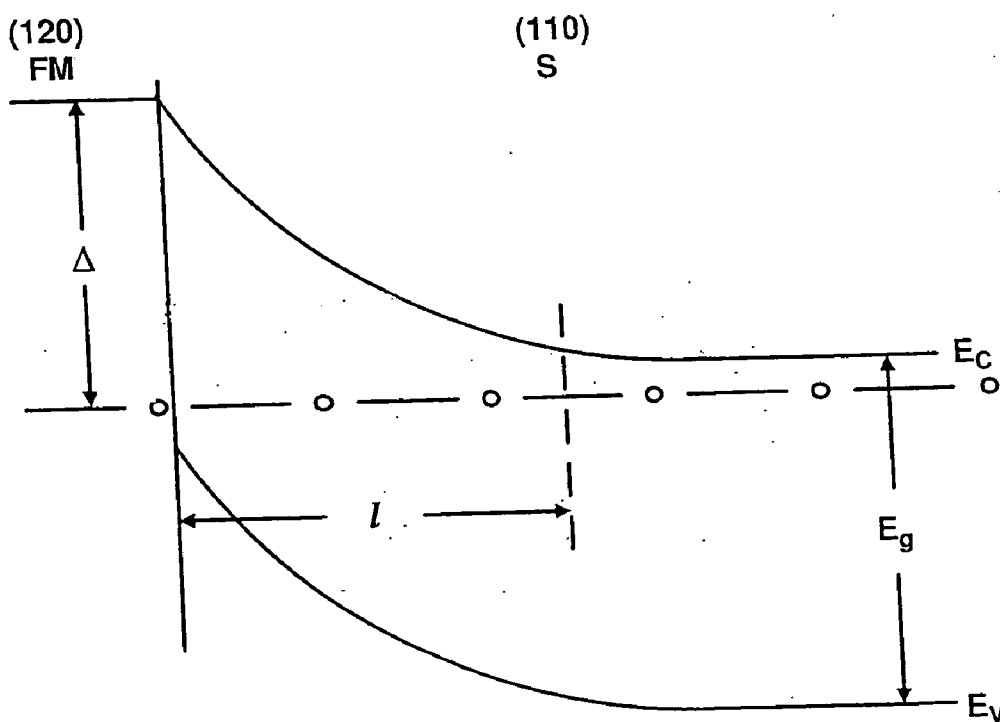


FIG. 1B

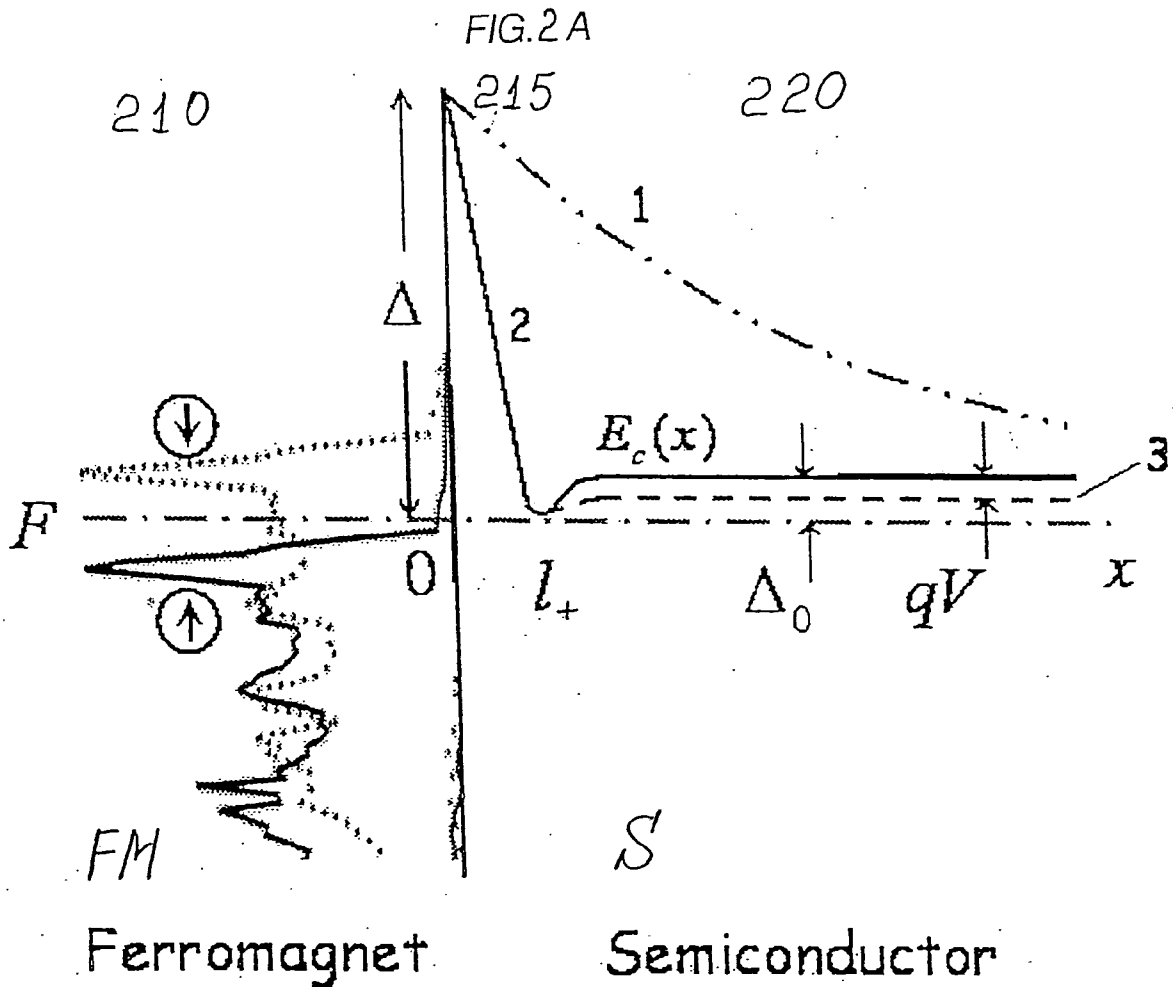
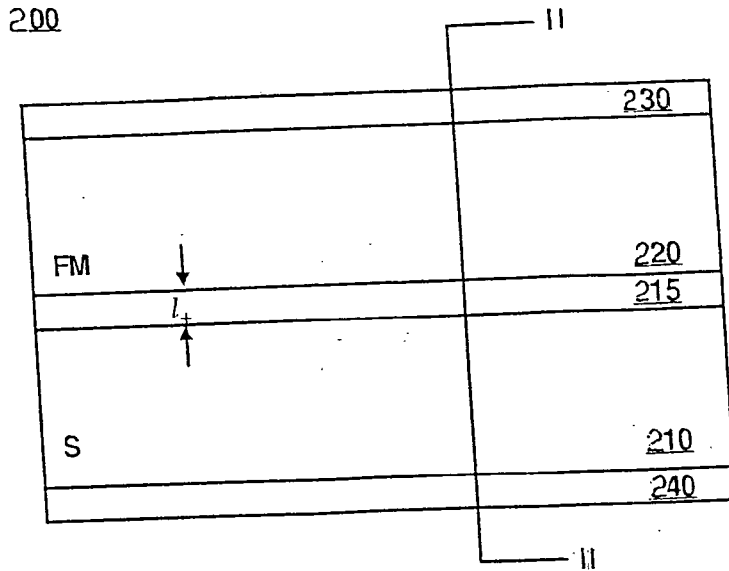


FIG. 2B

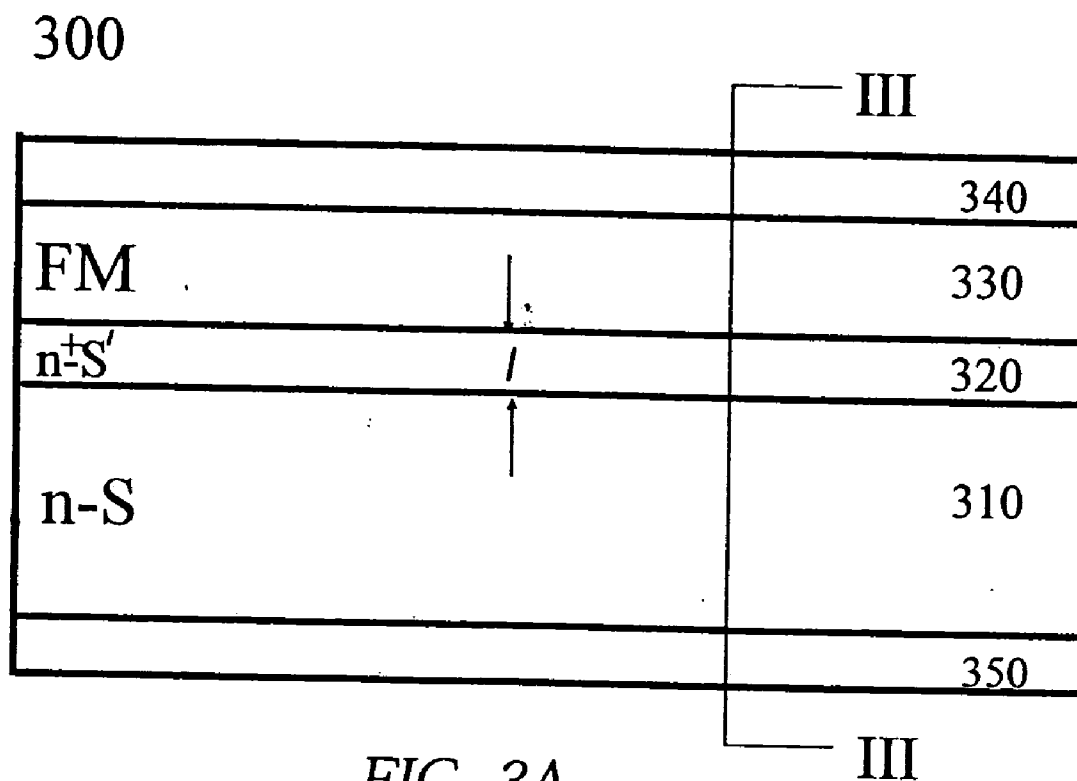


FIG. 3A

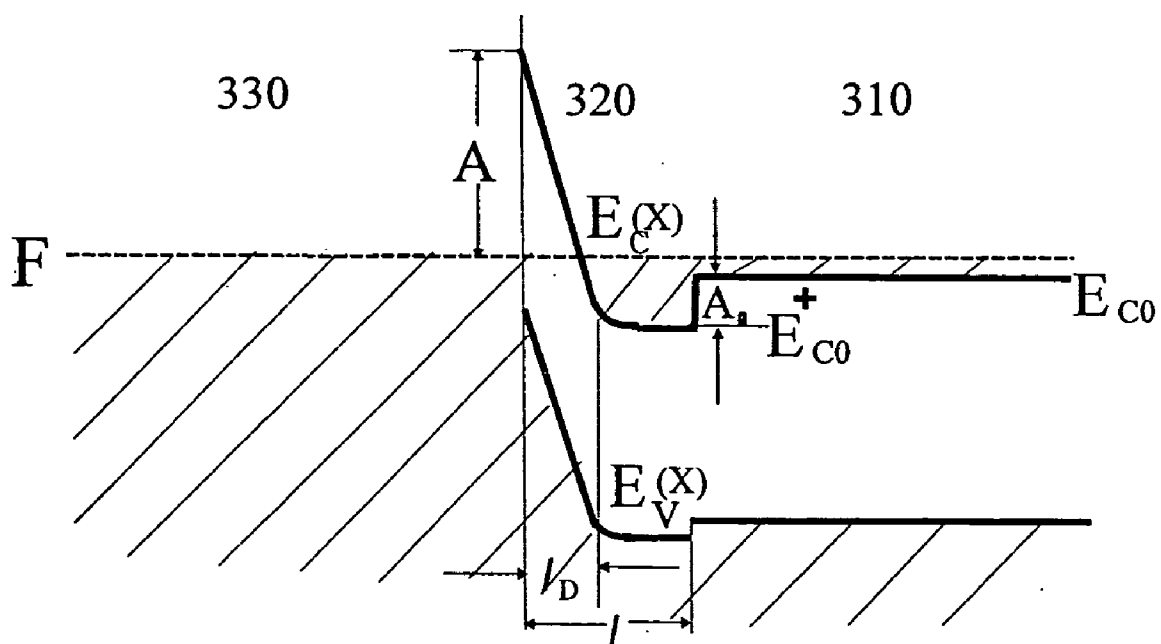


FIG. 3B

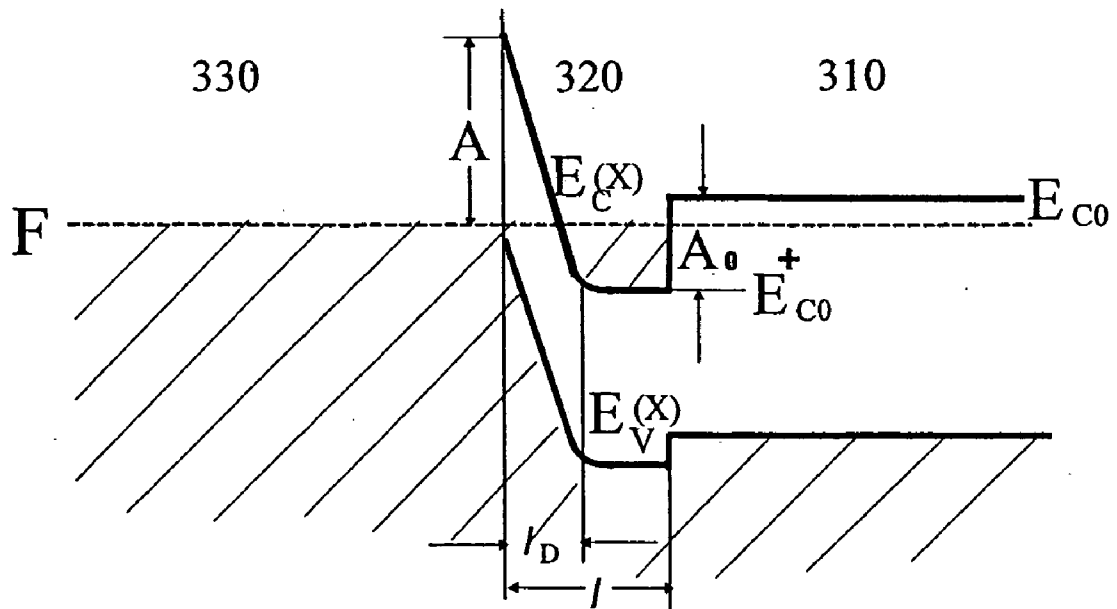


FIG. 3C

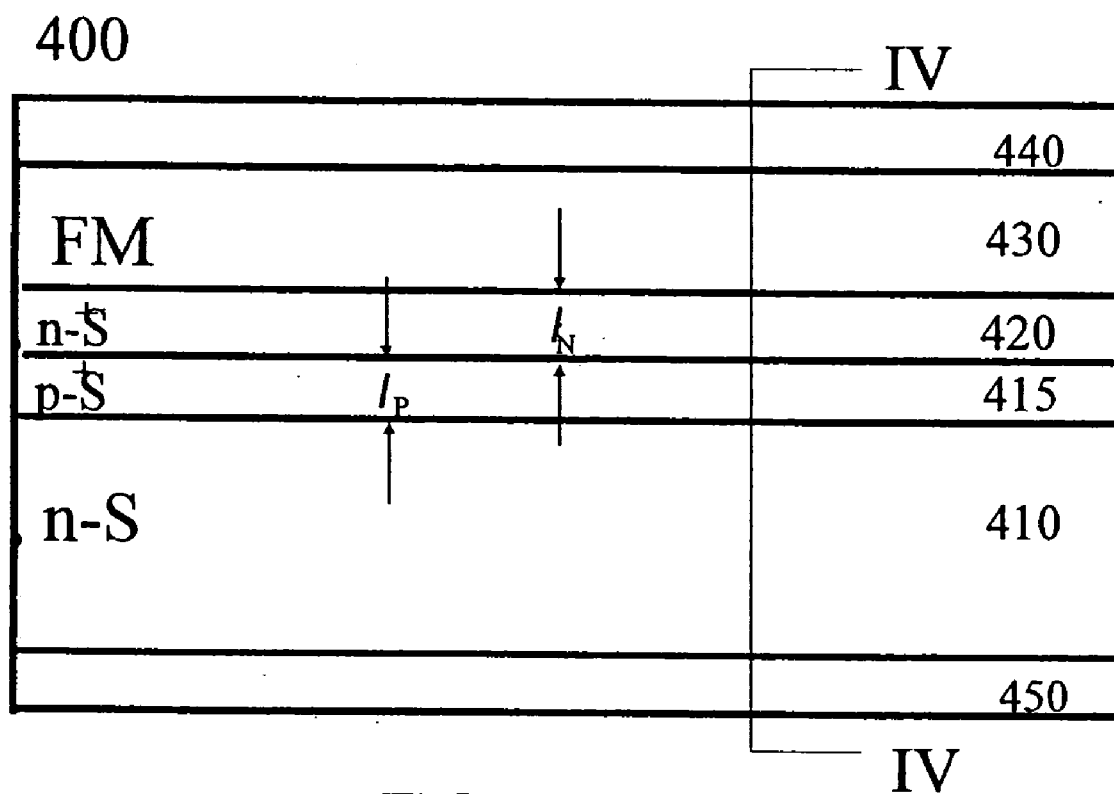


FIG. 4A

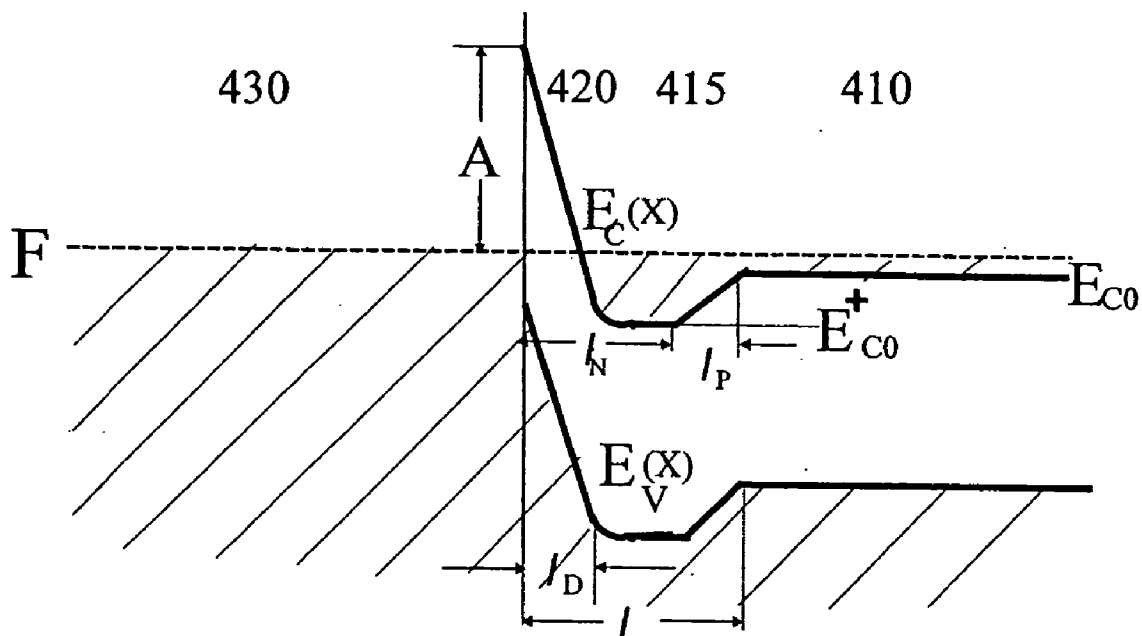


FIG. 4B

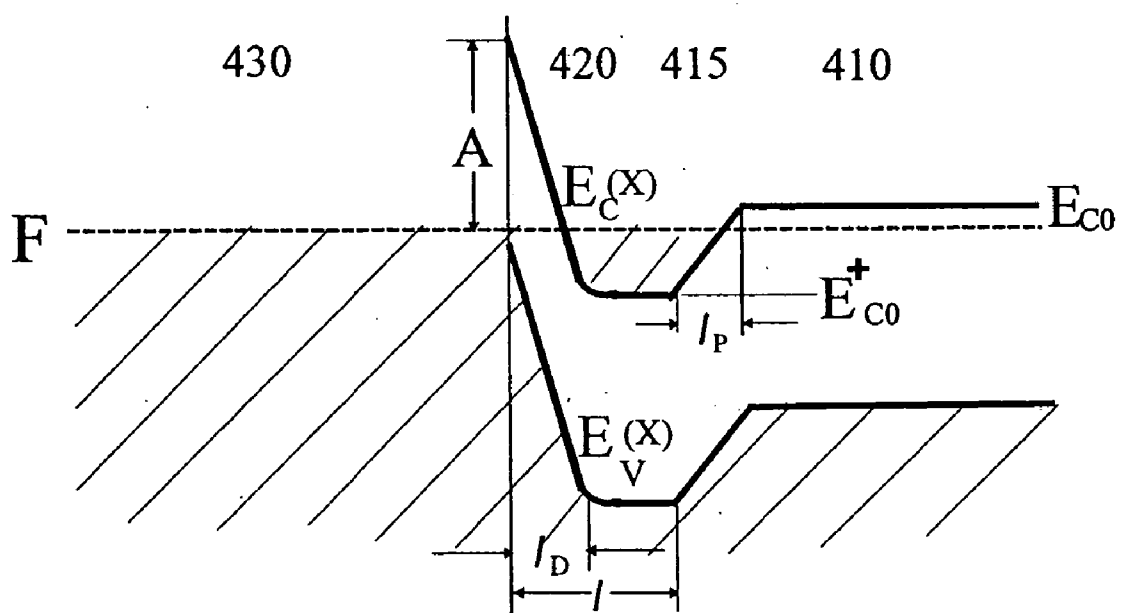


FIG. 4C

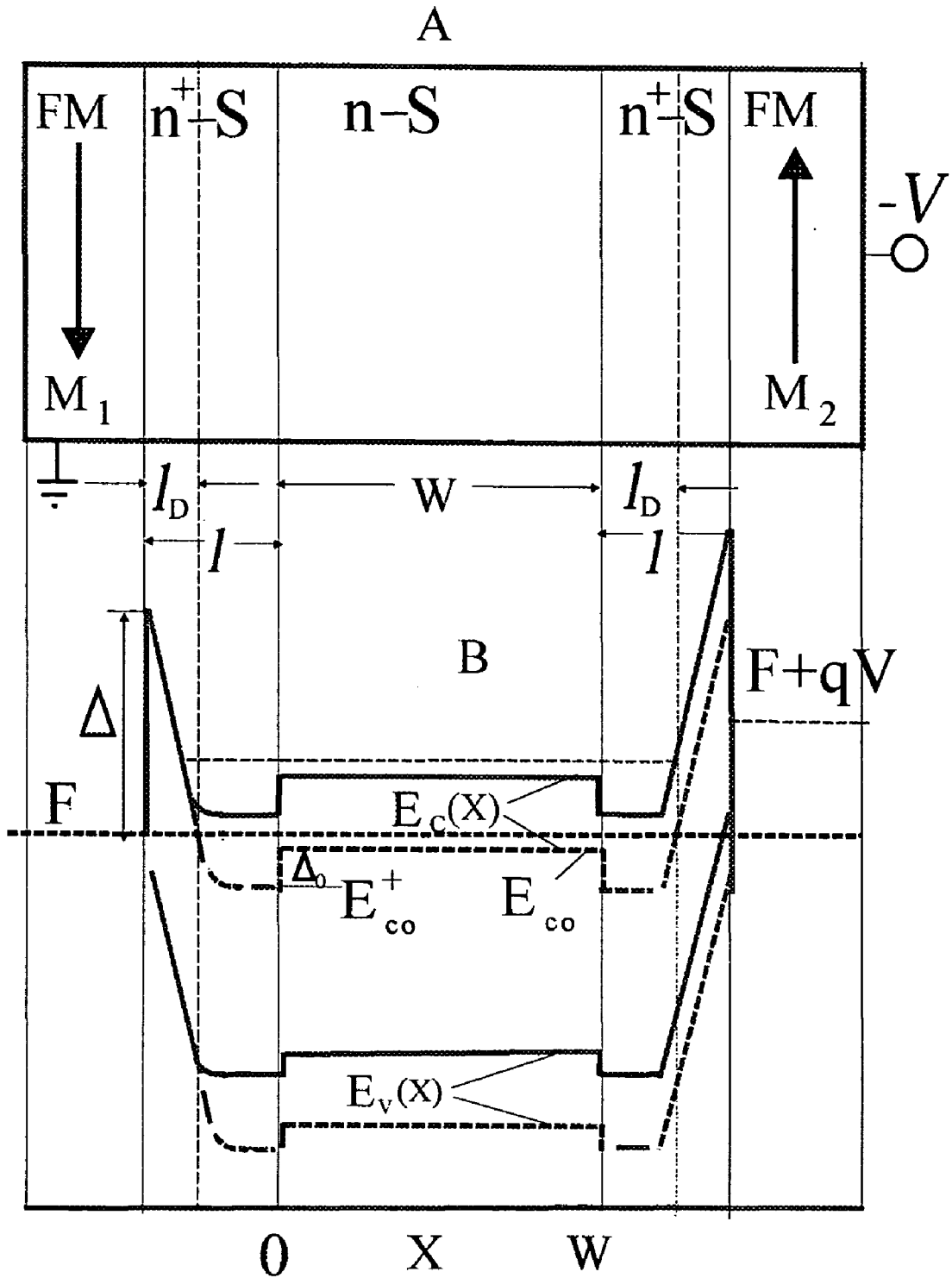


Figure 5A and 5B

**FERROMAGNETIC-SEMICONDUCTOR SPIN
POLARIZER OF ELECTRONS IN NONMAGNETIC
SEMICONDUCTORS**

BACKGROUND OF THE INVENTION

[0001] 1. Field of the Invention

[0002] In general, the present invention relates to spintronics. In particular, the present invention relates to creating a spin polarization of virtually all of the electrons in non-magnetic semiconductors at an arbitrary spin polarization current in ferromagnetic material and at a wide range of temperatures including room temperature.

[0003] 2. Description of the Related Art

[0004] The entire contents of each document listed below is expressly incorporated herein by reference:

[0005] [1] I. Zutic, J. Fabian, and S. Das Sarma, *Spintronics: Fundamentals and applications*, *Rev. Mod. Phys.* 76, 323 (2004).

[0006] [2] "Semiconductor Spintronics and Quantum Computation", edited by D. D. Awschalom, D. Loss, and N. Samarth (Springer, Berlin, 2002).

[0007] [3] M. Baibich, J. M. Broto, A. Fert, F. Nguyen Van Dau, F. Petroff, P. Eitenne, G. Creuzet, A. Friederich, and J. Chazelas, *Phys. Rev. Lett.* 61, 2472 (1988); R. E. Camley and J. Barnas, *Phys. Rev. Lett.* 63, 664 (1988); G. Binasch et al., *Phys. Rev. B* 39, 4828 (1989); J. Barnas et al., *Phys. Rev. B* 42, 8110 (1990); G. A. Prinz, *Science* 282, 1660 (1998).

[0008] [4] N. Garcia et al., *Phys. Rev. Lett.* 82, 2923 (1999); G. Tataru et al., *ibid.* 83, 2030 (1999); N. Garcia et al., *Phys. Rev. Lett.* 85, 3053 (2000); S. H. Chung et al., *ibid.* 89, 287203 (2002).

[0009] [5] S. Datta and B. Das, *Appl. Phys. Lett.* 56, 665 (1990); S. Gardelis, C. G. Smith, C. H. W. Barnes, E. H. Linfield, and D. A. Ritchie, *Phys. Rev. B* 60, 7764 (1999).

[0010] [6] R. Sato and K. Mizushima, *Appl. Phys. Lett.* 79, 1157 (2001); X. Jiang, R. Wang, S. van Dijken, R. Shelby, R. Macfarlane, G. S. Solomon, J. Harris, and S. S. P. Parkin, *Phys. Rev. Lett.* 90, 256603 (2003).

[0011] [7] A. M. Bratkovsky and V. V. Osipov, *Phys. Rev. Lett.* 92, 098302 (2004);

[0012] [8] V. V. Osipov and A. M. Bratkovsky, *Appl. Phys. Lett.* 84, 2118 (2004).

[0013] [9] V. V. Osipov, N. A. Viglin, and A. A. Samokhvalov, *Phys. Lett. A* 247, 353 (1998); Y. Ohno, D. K. Young, B. Beschoten, F. Matsukura, H. Ohno, and D. D. Awschalom, *Nature* 402, 790 (1999); R. Fiederling, M. Keim, G. Reuscher, W. Ossau, G. Schmidt, A. Waag, and L. W. Molenkamp, *ibid.* 402, 787 (1999).

[0014] [10] A. T. Hanbicki, B. T. Jonker, G. Itskos, G. Kioseoglou, and A. Petrou, *Appl. Phys. Lett.* 80, 1240 (2002); A. T. Hanbicki, O. M. J. van't Erve, R. Magno, G. Kioseoglou, C. H. Li, and B. T. Jonker, *ibid.* 82, 4092 (2003).

[0015] [11] H. Ohno, K. Yoh, K. Sueoka, K. Mukasa, A. Kawaharazuka, and M. E. Ramsteiner, *Jpn. J. Appl. Phys.* 42, L1 (2003).

[0016] [12] P. R. Hammar, B. R. Bennett, M. J. Yang, and Mark Johnson, *Phys. Rev. Lett.* 83, 203 (1999); Y. Ohno et al., *Nature* 402, 790 (1999); R. Fiederling et al., *Nature* 402, 787 (1999); M. Tanaka and Y. Higo, *Phys. Rev. Lett.* 87, 026602 (2001); H. J. Zhu, M. Ramsteiner, H. Kostial, M. Wassermeier, H.-P. Schonherr, and K. H. Ploog, *ibid.* 87, 016601 (2001); W. Y. Lee, S. Gardelis, B.-C. Choi, Y. B. Xu, C. G. Smith, C. H. W. Barnes, D. A. Ritchie, E. H. Linfield, and J. A. C. Bland, *J. Appl. Phys.* 85, 6682 (1999); T. Manago and H. Akinaga, *Appl. Phys. Lett.* 81, 694 (2002); A. F. Motsnyi, J. De Boeck, J. Das, W. Van Roy, G. Borghs, E. Goovaerts, and V. I. Safarov, *ibid.* 81, 265 (2002).

[0017] [13] S. M. Sze, *Physics of Semiconductor Devices* (Wiley, New York, 1981).

[0018] [14] V. V. Osipov and A. M. Bratkovsky, *Phys. Rev. B* 70, 205312 (2004); *Cond-mat/0307030* (2003).

[0019] [15] A. M. Bratkovsky and V. V. Osipov, *J. Appl. Phys.* 96, 4525 (2004).

[0020] [16] A. G. Aronov and G. E. Pikus, *Fiz. Tekh. Poluprovodn.* 10, 1177 (1976) [*Sov. Phys. Semicond.* 10, 698 (1976)].

[0021] [17] Z. G. Yu and M. E. Flatte, *Phys. Rev. B* 66, R201202 (2002).

[0022] [18] C. B. Duke, *Tunneling in Solids* (Academic, New York, 1969).

[0023] [19] A. M. Bratkovsky, *Phys. Rev. B* 56, 2344 (1997).

[0024] Over the past decade, new ventures in solid state electronic devices based on both the electron density and spin of electrons has led to the development of a new field: spintronics. Spintronics is the manipulation of electron spin in solid state materials. Spintronics creates the possibilities for designing ultra-fast, low-power scalable devices and applications for quantum computing. Among the practical spintronic effects is a giant magnetoresistance (GMR) in magnetic multilayers and tunnel ferromagnet-insulator-ferromagnet (FM-I-FM) structures. Discovery of GMR in magnetic multilayers has quickly led to important applications in storage technology. GMR is a phenomenon in which a relatively small change in a magnetic field results in a large change in the resistance of the material. The phenomenon of a large tunnel magnetoresistance (TMR) of FM-I-FM structures is being studied by product development teams in many leading companies. TMR is typically observed in FM-I-FM structures made of two ferromagnetic layers of similar or different materials, separated by an insulating thin tunnel barrier I, with thickness ranging between 1.4-2 nm. The tunnel current through the structure may differ significantly depending on whether the magnetic moment is parallel (low resistance) or anti parallel (high resistance). For example, in ferromagnets such as $\text{Ni}_{80}\text{Fe}_{20}$, Co-Fe, and the like, resistance may differ by up to 50% at room temperature for parallel (low resistance) versus antiparallel (high resistance) moments on ferromagnetic electrodes.

[0025] Recently, studies have been made in regard to giant ballistic magnetoresistance of Ni nano-contacts. Ballistic magnetoresistance is observed in Ni and some other nanowires in which the typical cross-section of the nano-contacts of the nanowire is a few square nanometers. The transport in

this case is through very short constriction and it is believed to be with conservation of electron momentum (ballistic transport). The change in the contact resistance can exceed 20-fold.

[0026] Of particular interest is injection of spin-polarized electrons into nonmagnetic semiconductors, because of the relatively large spin-coherence lifetime of electrons and the possibility of controlling the electron spin by external fields. The use of different ferromagnetic-semiconductor-ferromagnetic (FM-S-FM) heterostructures have recently been suggested, including those using an electric field, an external magnetic field, and a nanowire current. All the proposed spintronic devices are spin valves in which one of the ferromagnetic-semiconductor junctions works as a spin injector, and another one works as a spin polarizer (spin filter). Spin injection into nonmagnetic semiconductors (NS) holds promise both for the new generation of high-speed low-power electronic devices and quantum computing.

[0027] Relatively efficient spin injection in heterostructures with magnetic semiconductor as a spin source has been reported in {Refs. [9]}, the entire contents of which are expressly incorporated herein by reference. High enough spin injection from ferromagnets into nonmagnetic semiconductors has recently been demonstrated at low temperatures. However, the highest degree of spin polarization (the amount of electrons whose spin is coherent, or oriented the same) of injected electrons in nonmagnetic semiconductors, P_n , observed in all of existent works was less than 32% at low temperatures, and less than 10% at room temperatures. Thus far, all of the attempts to achieve higher spin polarization have faced fundamental difficulties.

[0028] The principal difficulty of the spin injection from a ferromagnetic (FM) into a nonmagnetic semiconductor is that a potential barrier (Schottky barrier) always arises in the semiconductor near the metal-semiconductor interface. Numerous experiments show that the barrier height Δ is determined by surface states forming on the interface, and is approximately $(\frac{2}{3}) E_g$, practically independently of the type of the metal. E_g is the energy band gap of the semiconductor, that is, the difference between the conduction band energy level E_c and the valence band energy level E_v . For example, for GaAs and Si the barrier height is equal to 0.5 eV-0.8 eV, with practically all metals, including Fe, Ni, and Co, and the barrier width, the length of the Schottky depleted layer l_D , is relatively large ($l_D \approx 40$ nm for doping concentration $N_d \approx 10^{17}$ cm^{-3}).

[0029] FIG. 1A illustrates a schematic of a conventional FM-S Schottky junction 100. As shown, the spin-injection junction 100 includes a semiconductor 110 and a ferromagnetic (FM) layer 120 above the semiconductor 110. The device 100 also includes electrodes 130 and 140 connected to the ferromagnetic layer 120 and the semiconductor 110. As will be described below, the Schottky barrier forms in such a way that the junction is very wide, which makes tunneling of electrons practically impossible.

[0030] FIG. 1B illustrates an energy band diagram of the conventional spin-injection device 100, illustrated in FIG. 1A. The barrier for electrons has a height Δ and width l (which is the thickness of the Schottky depletion layer).

[0031] The amount of spin injection from FM into NS materials is determined by the current in reverse direction

through the Schottky barrier, minus bias voltage applied to the FM (electron flow is directed from FM to semiconductor, and the current is directed to opposite direction). This current is usually extremely small, mainly due to the relatively large Schottky depleted layer l_D and $\Delta \gg k_B T$, where k_B is the Boltzmann constant and T is the device temperature. In the forward-biased FM-S (ferromagnetic semiconductor) Schottky junctions, a minus-bias voltage is applied to the semiconductor, and the current can reach a large value only at a bias voltage qV close to Δ , where q is the elementary charge.

[0032] Realization of an efficient spin polarization in nonmagnetic semiconductors (NS) due to such a thermoe-mission current is problematic for several reasons. First, electrons in FM_s with an energy $F+\Delta$ are weakly spin polarized, where F is the Fermi level. Second, according to standard theory, the thermionic current through Schottky junctions depends solely on the parameters of the semiconductor and not on the parameters of the metallic ferromagnet {13}. Therefore, the current could formally be spin-polarized in Schottky contacts. Thus, the effective spin injection in the conventional FM-S Schottky junction 100 is impossible for all practical purposes.

[0033] It has been proposed to use an ultrathin heavily doped semiconductor layer (δ -doped layer) between the FM material and a nondegenerate nonmagnetic semiconductor to increase the spin injection at room temperature, as shown in FIG. 2A. This δ -doped layer of a thickness $l_d \approx 1$ nm sharply reduces the thickness of the Schottky barrier, and increases its tunneling transparency. According to {14, "EFFICIENT NONLINEAR ROOM-TEMPERATURE SPIN INJECTION FROM FERROMAGNETS INTO SEMICONDUCTORS THROUGH A MODIFIED SCHOTTKY BARRIER", V. V. Osipov and A. M. Bratkovsky, Phys. Rev. B 70, 205312 (2004); cond-mat/0307030 (2003).}, the entire contents of which are expressly incorporated herein by reference, the thickness of the δ -doped layer, l_d , should be on the order of a typical tunneling length for the barrier $l_d \approx \lambda_0$ where

$$l_0 = (\hbar^2 / 8\pi^2 m^* \Delta)^{1/2} \quad (1)$$

And m^* is the effective mass of electrons in the semiconductor δ -doped layer. Moreover, the bottom of conduction band, E_{c0} , in the semiconductor δ -doped layer and the nonmagnetic in equilibrium should be higher than the Fermi level, $E_{c0} > F$. The semiconductor has to be nondegenerate in whole semiconductor region including the δ -doped layer, as shown in FIG. 2B. It has been shown theoretically that spin polarization in the nonmagnetic semiconductor, P_n , both in the reverse-biased and forward-biased FM-S junction can approach that in the FM material at an electron energy of $E \approx E_c$. In the reverse-biased FM-S junction, the predictable value of P_n can achieve a maximum, $P_{n\max}$, at room temperatures when E_c corresponds to a spike of density of minority electron states in the FM material, as shown in FIG. 2B. Even a theoretical value of $P_{n\max}$, however is substantially smaller than 1 at room temperatures and $P_n = 0$ at low temperatures. In the forward-biased FM-S junction, $P_{n\max}$ is determined by spin polarization of current in the FM material which is substantially smaller than 1 (by about 30%-40%) {see [15]}.

[0034] Characteristics of all of spintronic devices improve dramatically with increase in the degree of the electron spin

polarization, P_n , and achieve to them, limited values when $P_n \nabla 1$ (100%). Moreover, a fundamental problem for quantum computing is to obtain an electron spin polarization in nonmagnetic semiconductors (NS) of $P_n=100\%$ at very low temperatures, such as, $T < 1^\circ \text{K}$.

SUMMARY OF THE INVENTION

[0035] An object of the present invention is to substantially solve at least the above problems and/or disadvantages and to provide at least the advantages described below. Accordingly, it is an object of the present invention to provide a spin polarizer comprising a semiconductor, a ferromagnetic layer, and a thin degenerate semiconductor layer formed between the ferromagnetic layer and the semiconductor which is more highly doped than the semiconductor layer. The concentration of shallow donors N_d^+ in this layer satisfies the condition: $4\pi N_d^+ a_B^3 / 3 > 1$, where a_B is Bohr radius of the shallow donor; the thickness, l , of this layer satisfies a condition: $6l_0 \leq 2l_d \leq l < L_{S^+}$ wherein:

[0036] l_0 represents a tunneling length for a Schottky barrier between the ferromagnetic layer and the thin degenerate semiconductor layer given by the equation $l_0 = (\hbar^2 / 8\pi^2 m^* \Delta)^{1/2}$

[0037] l_D represents a thickness of the Schottky barrier given by $l_D = (2\epsilon\epsilon_0 \Delta / q^2 N_d^+)^{1/2}$ wherein ϵ_0 is the permittivity of free space, ϵ is the relative permittivity of the thin degenerate semiconductor layer, $q > 0$ is the elementary charge, and N_d^+ is the concentration of shallow donors in the highly doped degenerate semiconductor layer, Δ is a height of the Schottky barrier; and

[0038] L_{S^+} represents a length of electron spin relaxation in the thin degenerate semiconductor layer given by the $L_{S^+} = \sqrt{D^+ \tau_s^+}$ where D^+ and τ_s^+ are diffusion coefficient and time of spin coherence of electrons in the thin degenerate semiconductor n^+ -S layer, respectively.

[0039] According to an embodiment of the present invention, efficient spin polarizer of electrons in nonmagnetic semiconductors that are ferromagnetic-semiconductor heterostructures comprises a magnetic semiconductor layer or ferromagnetic metal layer, a nonmagnetic semiconductor, and a thin high doped degenerate semiconductor layer, satisfying certain requirements and situated between the ferromagnetic layer and the nonmagnetic semiconductor. The spin polarizer ensures spin polarization of electrons in the nonmagnetic semiconductor at substantially 100% near the ferromagnetic-semiconductor junctions at temperatures ranging from very low temperatures ($T < 1^\circ \text{K}$.) to room temperatures.

BRIEF DESCRIPTION OF THE DRAWINGS

[0040] The various objects, advantages and novel features of the present invention will be best understood by reference to the detailed description of the preferred embodiments that follow, when read in conjunction with the accompanying drawings, in which:

[0041] FIG. 1A illustrates a schematic of a conventional ferromagnetic metal-semiconductor Schottky junction;

[0042] FIG. 1B illustrates an energy band diagram of the conventional Schottky junction 100 illustrated in FIG. 1A along the line I-I;

[0043] FIG. 2A illustrates a schematic of an earlier proposed spin injection device that is a modified Schottky ferromagnetic-semiconductor junction with an ultrathin high doped layer, the δ -doped layer, between the ferromagnet and semiconductor;

[0044] FIG. 2B illustrates an energy diagram of the spin injection device shown in FIG. 2A along the line II-II;

[0045] FIG. 3A illustrates an exemplary variant of a spin polarizer according to an embodiment of the present invention based on a ferromagnetic-semiconductor heterostructure including a thin high negative doped degenerate semiconductor layer, n^+ -S layer that is situated between the ferromagnetic layer and negative doped semiconductor (n -S region), and has a narrower energy bandgap than that of the n -S region;

[0046] FIG. 3B illustrates an exemplary energy diagram of spin polarizer shown in FIG. 3A along the line III-III in the case when the n -S region is a degenerate semiconductor;

[0047] FIG. 3C illustrates an exemplary energy diagram of spin polarizer shown in FIG. 3A along the line III-III in the case when the n -S region is a nondegenerate semiconductor;

[0048] FIG. 4A illustrates an exemplary variant of a spin polarizer according to an embodiment of the present invention based on a ferromagnetic-semiconductor heterostructure including two thin negative and positive high doped degenerate semiconductor layers, n^+ -S and p^+ -S, which are situated between the ferromagnet and negative doped semiconductor n -S region, and have the same energy bandgap than that of the n -semiconductor region;

[0049] FIG. 4B illustrates an exemplary energy diagram of spin polarizer shown in FIG. 4A along the line IV-IV in the case when the n -S region is a degenerate semiconductor;

[0050] FIG. 4C illustrates an exemplary energy diagram of spin polarizer shown in FIG. 4A along the line IV-IV in the case when the n -S region is a nondegenerate semiconductor;

[0051] FIG. 5A illustrates an exemplary variant of a spin device according to an embodiment of the present invention based on a ferromagnetic-semiconductor layer localized between two ferromagnetic metal layers; and

[0052] FIG. 5B illustrates an exemplary energy diagram corresponding to the spin device of FIG. 5A.

[0053] Throughout the drawings, like reference numbers will be understood to refer to like elements, features and structures.

DETAILED DESCRIPTION OF EXEMPLARY EMBODIMENTS

[0054] Several embodiments of the present invention will now be described in detail with reference to the annexed drawings. In the following description, detailed descriptions of known functions and configurations incorporated herein have been omitted for conciseness and clarity.

[0055] For simplicity and illustrative purposes, the principles of the present invention are described by referring mainly to exemplary embodiments thereof. In the following description, numerous specific details are set forth in order

to provide a thorough understanding of the present invention. Those with skill in the art will recognize that various changes and modifications can be made to the examples provided herein without departing from the scope and spirit of the invention.

[0056] The exemplary embodiments of the present invention are a spin polarizer that in the general case, can contain a ferromagnetic-semiconductor (FM-S) junction ensuring a spin polarization of current, P_j , near a boundary with a nonmagnetic semiconductor (NS) depending relatively weakly on the current. The spin polarizer can create spin polarization of electrons virtually to 100%, inside some areas of the nonmagnetic semiconductor (NS) near the FM-S junction. This occurs when the electrons drift under the action of a strong enough electrical field from the NS into the FM even in the case when P_j is relatively small (P_j can be even ~5%-15%).

[0057] FIG. 3A illustrates a spin polarizer 300 according to an embodiment of the present invention. As will be discussed below, the device 300 can ensure spin polarization of electrons is equal virtually to 100% inside some area of the nonmagnetic semiconductor (NS) near the FM-S junction. The spin polarizer 300 includes a nonmagnetic semiconductor (NS) layer 310, a thin, highly doped degenerate semiconductor layer n+-S'320, above the semiconductor 310 and a ferromagnetic (FM) layer 330 above the semiconductor layer 320. The spin polarizer 300 can also include electrodes 340 and 350 electrically connecting the ferromagnetic layer 330 and the NS layer 310, respectively. The spin polarizer 300 thus formed is described as having an FM-n+-S'-n-S heterostructure with parameters adjusted to sufficiently meet the conditions discussed below.

[0058] The FM layer 330 can be formed from various magnetic materials, preferably Ni, Fe and Co, as well as various magnetic alloys, which can include one or a combination of Fe, Co, and Ni. The NS layer 310 can be formed from various semiconductor materials including any one of Si, GaAs, ZnTe, GaSb, GaP, Ge, InAs, CdSe, InP, InSb, CdTe, CdS, ZnS, ZnSe, AlP, AlAs, AlSb, and also alloys of these materials. In an exemplary embodiment of the present invention, the semiconductor 310 can be formed from semiconductor materials with relatively large electron spin relaxation time, L_s . These include, for example GaAlAs, InAs, ZnSe and ZnCdSe among others. The NS layer 310 can be negatively doped. Negative dopant metals that can be used include P, As, Sb for Si and Ge, and Ge, Se, Te, Si, Pb and Sn for GaAs.

[0059] The high doped semiconductor, the n+-S' layer 320, may be formed from various semiconductor materials having an energy bandgap narrower than that for the semiconductor 310. For example, the n-dopant metals may be P, As, Sb for Si and Ge, and Ge, Se, Te, Si, Pb and Sn for GaAs. The thin degenerate semiconductor layer 320 may be used to increase tunneling transparency of the Schottky barrier for electrons with energies $E > F$ and to ensure a spin polarization of current near the FM-n+-S' junction weakly depending on the current. The parameters of the n+-S' layer 320 should be satisfied by certain conditions listed below.

[0060] FIG. 3B illustrates an exemplary energy diagram of spin polarizer 300 along the line III-III of FIG. 3A in the case when the n-S region 310 is a degenerate nonmagnetic semiconductor. The device 300 can be used at an arbitrary

temperature and can ensure spin polarization of electrons close to 100% in an area of the nonmagnetic semiconductor near the boundary with the n+S layer 320. F is the Fermi level; Δ and l_D are the height and thickness of the Schottky barrier of the FM-n+-S' junction, l is the thickness of the highly doped n+S' layer 320, Δ_0 is a jump at the bottom of the conduction band, $E_c(x)$, at the boundary of the n+S' layer 320 and the semiconductor 310.

[0061] FIG. 3C illustrates an exemplary energy diagram of spin polarizer 300 along the line III-III of FIG. 3A in the case when the n-S region 310 is a nondegenerate nonmagnetic semiconductor. The device 300 can advantageously be used at relatively high temperatures, including room temperatures, and may ensure spin polarization of electrons close to 100% in the nonmagnetic semiconductor near the boundary with the n+S layers 320. F is the Fermi level; Δ and l_D are the height and thickness of the Schottky barrier at the FM-n+S interface, l is the thickness of the highly doped n+S' layer 320, Δ_0 is a jump at the bottom of the conduction band, $E_c(x)$, at the boundary of the n+-S' layer 320 and the semiconductor 310.

[0062] The FM-n+S' junction shown in FIGS. 3A-3C at certain parameters presented below ensures a spin polarization of current, P_{j1} , near the boundary between the n+-S' layer 320 and the semiconductor 310, the point $x=l$, weakly depending on the current. These conditions ensure the 100% spin polarization of electrons in some area of the semiconductor 310 near the boundary even for a relatively small value of P_{j1} as will be described in further detail below.

[0063] The currents of electrons with spin $\sigma = \uparrow, \downarrow$ in NS are given by the following equations (See, for example, Ref's [7][8][16], and [17]):

$$J_{\sigma} = q\mu_{\sigma}E + qD(dn_{\sigma}/dx), \quad (2)$$

$$\delta J_{\sigma}/dx = q\delta n_{\sigma}/\tau_s, \quad (3)$$

where D , μ and τ_s are the diffusion constant, mobility and spin-coherence lifetime of the electrons, respectively, and E the electric field. From conditions of continuity of the total current $J(x) = J_{\uparrow} + J_{\downarrow} = \text{const}$ and quasineutrality

$$n(x) = n_{\uparrow} + n_{\downarrow} = n_s \quad (4)$$

it follows that

$$E(x) = J/q\mu n_s = \text{const} \quad (5)$$

and

$$\delta n_{\downarrow}(x) = n_{\downarrow} - n_s/2 = -\delta n_{\uparrow}(x) \quad (6)$$

where n_s is total electron density in the semiconductor 310. From Equations (2) through (6), it follows that spin density in the semiconductor 310, that is, for $x \geq l$ can be written as:

$$\delta n_{\uparrow}(x) = \delta n_{\uparrow 1} \exp[-(x-l)/L] = n_s/2 \exp[-(x-l)/L] \quad (7)$$

where

$$L = (1/2) \{ [(L_E)^2 + (2L_s)^2]^{1/2} - (\pm)L_E \} = L_s \{ [(1 + (J/2J_s)^2)]^{1/2} - J/2J_s \}, \quad (8)$$

$$L_s \sqrt{D\tau_s} \text{ and } L_E = \mu\tau_s|E| = L_s J/J_s \quad (9)$$

are the diffusion and drift lengths of electron spin, respectively; the index \pm corresponds to the forward, $J > 0$, and reverse bias voltage, $J < 0$, respectively. Here we introduce a typical current:

$$J_s = qn_s D/L_s = qn_s L_s/\tau_s \quad (10)$$

and spin polarization of electrons in the semiconductor **310** (for $x \geq 1$)

$$P_{n\uparrow} = (\delta n_{\uparrow} - \delta n_{\downarrow}) / n_S = P_{n\uparrow} \exp[-(x-1)/L], \quad (11)$$

where

$$P_{n\uparrow} = P_{n\uparrow}(l) = (\delta n_{\uparrow} - \delta n_{\downarrow}) / n_S = 2\delta n_{\uparrow} / n_S \quad (12)$$

is spin polarization of electrons at the boundary of the semiconductor **310** (at the point $x=1$, **FIG. 1**). From Equations (2) and (7) the spin currents at the point $x=1$ is

$$J_{\uparrow, \downarrow} = (J/2) \approx (J_S/2) (L/L_S) P_{n\uparrow} \quad (13)$$

From Equation (13) it follows that the relationship between the spin polarization of current

$$P_{L\uparrow} = (J_{\uparrow} - J_{\downarrow}) / (J_{\uparrow} + J_{\downarrow}) = (J_{\uparrow} - J_{\downarrow}) / J \quad (14)$$

and the spin polarization of electrons, $P_{n\uparrow}$, at the point $x=1$

$$P_{n\uparrow} = -P_{J\uparrow} (J_L / J_S) \quad (15)$$

($P_{J\uparrow}$ is also called spin injection coefficient of the FM-n \pm S' contact.)

[0064] According to Equation (8) $L=L_S$ at $J \ll J_S$ therefore, as expected $P_{n\uparrow} \approx 1-J$ at $P_{J\uparrow} > 0$ (in certain cases $P_{J\uparrow} < 0$). In the reversed-biased FM-S junctions, $J < 0$, according to (15) the value of $P_{J\uparrow} = 2\delta n_{\uparrow} / n_S > 0$ ($\delta n_{\uparrow} > 0$), that is, the accumulation of electrons with spin $\sigma = \uparrow$ is realized in the semiconductor **310** near the boundary with the FM-S junction. At $|J| > J_S$ the spin penetration depth L (8) increases with current J and $P_{n\uparrow} \rightarrow P_{J\uparrow}$ at $|J| \gg J_S$. Thus, the spin polarization of electrons in the semiconductor injected from FM can achieve spin polarization of current in the reversed-biased FM-S junction.

[0065] Another situation is realized in the forward-biased FM-S junctions, $J > 0$, when electrons drift under the action of the electric field from the semiconductor into FM. Here the value $\delta n_{\uparrow} < 0$ and $\delta n_{\downarrow} > 0$ at $P_{J\uparrow} > 0$, that is, electrons with spin $\sigma = \uparrow$ are extracted from NS and electrons with spin $\sigma = \downarrow$ are accumulated in the NS. The opposite situation is realized at $P_{J\uparrow} < 0$. At $J > J_S$ the spin penetration depth L (Equation (8)) decreases with current J and according to Equation (15) $|P_{n\uparrow}|$ rises to 1 (100%) at:

$$J = J_S [P_{J\uparrow} / (1 + |P_{J\uparrow}|)]^{-1/2} \quad (16)$$

when

$$L = L_S [P_{J\uparrow} / (1 + |P_{J\uparrow}|)]^{1/2} \quad (17)$$

[0066] Thus, spin polarization of electrons in the semiconductor near the forward-biased FM-S junction achieves 100% even at relatively small spin polarization of current, $P_{J\uparrow}$, in the FM-S junction. This is valid both for a degenerate semiconductor **310**, as shown in **FIG. 3B**, and for a nondegenerate semiconductor **310**, as shown in **FIG. 3C**. The value of $P_{J\uparrow}$, determines the threshold current, J_c , and spin penetration depth, L_c , but it does not change the main result:

$$|P_{n\uparrow}| = 1 \text{ at } J = J_c \quad (18)$$

[0067] The one requirement is a weak dependence of the spin polarization of current in the FM-S junction (or spin injection coefficient) $P_{J\uparrow}$, on the current J . We note that when the current $J > J_c$ the value $|P_{n\uparrow}| = 2|\delta n_{\uparrow}| / n_S = 2n_{\uparrow} = n_S / n_S$ becomes formally more than 1, that is, the density of electrons n_{\uparrow} or n_{\downarrow} with spin $\sigma = \downarrow$ or $\sigma = \uparrow$ at the point $x=1$ becomes more than the total electron density n_S . This means that the condition of quasineutrality (4) is violated and a negative space charge arises near the boundary of the semiconductor with the FM-S junction, $x=1$ in **FIG. 1**. This charge will decrease the ohmic field $E = J / qn_S$ (5), and

consequently the drift spin length $L_E = \mu \tau_s |E|$. As a result the spin penetration depth L (Equation (8)) stops decreasing with current J and the values of the spin density $P_{n\uparrow}$ and L are stabilized near $|P_{n\uparrow}| = 1$ and $L = L_c$, respectively.

[0068] Thus, embodiments of the present invention provide FM-S junctions which have the spin polarization of $P_{J\uparrow}$, weakly depending on the current J in the junctions. This requirement is valid, in particular, for the FM-n \pm S' junction shown in **FIGS. 3A-3C** when the n \pm S' layer **320** is a degenerate semiconductor and has the thickness, l , of the n \pm S' layer **320** that satisfies the condition:

$$3l_D < l < L_S^+ \quad (19)$$

wherein $L_S^+ = \sqrt{D^+ \tau_s^+}$, D^+ and τ_s^+ are the length of electron spin, diffusion constant, and the spin-coherence lifetime of the electrons in the n \pm S' layer **320**, respectively; l_D is the width of the Schottky depletion layer in the n-semiconductor **320** near the FM-n \pm S' interface which is represented by the following equation:

$$l_D = (2\epsilon \epsilon_0 \Delta / q^2 N_d^+)^{1/2} \quad (20)$$

wherein ϵ_0 is the permittivity of free space, ϵ is the relative permittivity of the n \pm -semiconductor **320**, $q > 0$ is the elementary charge, and N_d^+ is the concentration of shallow donors in the highly doped n \pm S' layer **320**. The part of the n \pm S' layer **320**, corresponding to x such that $l_D < x < l$, shown in **FIG. 3B**, should be a degenerate semiconductor which has the electron density $n^+ = N_d^+$. This means that $n^+ = N_d^+$ should correspond to a case when the Fermi level $F > E_{c0}^+$ in this part, as shown in **FIGS. 3B and 3C**. This is realized when $l > 3l_D$ and the donor concentration N_d^+ satisfies the condition $4\pi N_d^+ a_B^3 / 3 > 1$, where a_B is the Bohr radius of the shallow donor. The donor concentration N_d^+ in the n \pm -S' layer **320** has to be much more than a donor concentration N_d in the n-semiconductor **310** which can be either a degenerate semiconductor, as shown in **FIG. 3B**, or a nondegenerate semiconductor, as shown in **FIG. 3C**. Moreover, the following condition should be satisfied:

$$l_D > l_0 \quad (21)$$

where l_0 is given by Eq. (1). More precise requirements for the parameters l_D , l and N_d^+ are presented below.

[0069] Because of the very high density of electrons in the FM metal **330** and the degenerate semiconductor layer **320** the tunneling current through the FM-n \pm S' layer is determined by the well-known formula (See, for example, Ref.'s [18] and [19]):

$$J_{\sigma 0} = \frac{q}{h} \int dE [f(E - F) - f(E - F)] \int \frac{d^2 k_{\parallel}}{(2\pi)^2} T_{\sigma} \quad (22)$$

where k_{\parallel} is the component of the wave vector k parallel to the FM-S interface, $f(E - F)$ the Fermi function, V is a bias voltage and T_{σ} is the tunneling transmission probability of the FM-n \pm -S' junction.

[0070] The value of T_{σ} may be estimated in a semiclassical approximation (WKB) (See, for example, Ref.'s [14] and [15]) as follows:

$$T_{\sigma} = \frac{16\alpha v_{\alpha x} v_x}{v_{\alpha x}^2 + v_x^2} \exp(-\eta \kappa l_D) \quad (23)$$

[0071] where $v_{\alpha x}$ is the x-component of velocity v_{α} of electrons with the wave vector k and spin σ in a direction of current; $\kappa = (\Delta + F - E + E_{II})^{3/2} [\Delta(\Delta - qV)l_0]^{-1}$, $v_{xt} = \hbar k / 2\pi m^*$ is the "tunneling" velocity; $E_{II} = \hbar^2 k_{II}^2 / 8\pi^2 m^*$; m^* and

$v_x = [2(E - E_{c0} - qV - E_{II}) / m^*]^{1/2}$ are an effective mass and x-component of the velocity of electrons in n+-semiconductor layer 320

[0072] ; $\alpha = 1.2(\kappa l_D)^{1/3}$ and $\eta = 4/3$ or $\alpha = 1$ and $\eta = 2$ for the Schottky barrier of triangular or rectangular shape, respectively. The real shape of the barrier is not significant. For definiteness we consider the case when the temperature $k_B T \ll \mu^+ s = (F - E_{c0})$.

[0073] Taking into account that the velocity v_x is real only at $E > E_{c0} + qV + E_{II}$ and also a property of the Fermi function at $k_B T \ll \mu^+ s$ one can find from Equations (22) and (23) that the spin current at the FM-n+-S' interface, at the point $x=0$ in FIG. 1, can be written at $qV \leq \mu^+ s$ as:

$$J_{\sigma 0} = q n_s^+ v_{\mu} \alpha_0 T_t(V) d_{\sigma} [1 - (1 - qV / \mu_s^+)^{5/2}], \quad (24)$$

$$T_t(V) = \exp\left[-\frac{\eta l_D (\Delta - qV)^{1/2}}{l_0 \Delta^{1/2}}\right], \quad (25)$$

$$d_{\sigma} = \frac{v_{\mu} v_{\sigma 0}}{v_{\sigma 0}^2 + v_{\sigma}^2}, \quad (26)$$

wherein $v_{\sigma 0} = v_{\sigma}(F + qV)$ and $v_{\mu} = (3\mu_s^+ / m^*)^{1/2}$ are velocities of electrons with spin σ and the energies $E = F + qV$ and μ_s^+ in FM and n+-S' layers 330 and 320, respectively; $v_{\sigma 0} = (2(\Delta - qV) / m^*)$. From Equations (24)-(26) it follows that the total current $J = J_{\uparrow 0} + J_{\downarrow 0}$ is equal to:

$$J = J_0 d_{\sigma} [1 - (1 - qV / \mu_s^+)^{5/2}], \quad (27)$$

wherein

$$J_0 = dq n_s^+ v_{\mu} \alpha_0 T_t(V) \quad (28)$$

$$d_{\sigma} = (d_{\uparrow} + d_{\downarrow}) \quad (28)$$

and the spin polarization of current, P_{J_0} , at the FM-n+-S' interface is equal to:

$$P_{J_0} = \frac{J_{\uparrow 0} - J_{\downarrow 0}}{J_{\uparrow 0} + J_{\downarrow 0}} = \frac{(v_{\uparrow 0} - v_{\downarrow 0})(v_{\uparrow 0}^2 - v_{\downarrow 0} v_{\uparrow 0})}{(v_{\uparrow 0} + v_{\downarrow 0})(v_{\uparrow 0}^2 + v_{\downarrow 0} v_{\uparrow 0})} \equiv P_F \quad (29)$$

[0074] The expression for $P_{J_0} = P_F$ coincides with that for spin polarization of current in usual tunneling FM-I-FM structures [18,19]. One can see that $P_{J_0} = P_F$ does not depend on the current. When the thickness of the n+-S' layer $l \ll L_S^+$, where $L_S^+ = (D^+ \tau_S^+)^{1/2}$ and τ_S^+ are the length and relaxation time of electron spin in the n+-S' layer, but $l > l_D$, spin currents in the n+-S' layer do not change practically, therefore we can put $J_{\sigma 0} = J_{\sigma l}$ and $P_{J_0} = P_{Jl}$ where P_{Jl} is the spin polarization of the current at the boundary between the n+-S'

layer 320 and the n-S region 310. By analogy with Equation (13) the spin current in the n+-S' layer is equal to

$$J_{\{\uparrow, \downarrow\}} = (J/2) \approx (J_S^+ / 2) P_{nl}^+, \quad (30)$$

wherein $P_{nl}^+ = 2\delta n^+ / n^+$ is the spin polarization of electrons in the n±S' layer changing with the typical length L_S^+ and the typical current is:

$$J_S^+ = q D^+ n^+ s / L_S^+, \quad (31)$$

where n^+ is the electron density in the degenerated region of the n±S' layer.

[0075] Therefore for arbitrary l the value of $P_{Jl} = P_F / \cos h(l/L_S^+)$. Thus, $P_{Jl} \approx P_F$ when $l \ll L_S^+$ in the considered FM-n+-n-S heterostructure shown in FIGS. 3A-3C. P_{Jl} very weakly depends on the current, therefore according to Equations (15) and (18) the spin polarization electrons in the forward-biased FM-n+-n-S heterostructure near in the n-S region $P_{n0} = 1$ (100%) at the threshold current and spin penetration depth in the n-S region is determined by Eqs. (16) and (17). Eqs. (16) and (17) are valid when the thickness of the n±S' layer

$$l \gg L_S (\beta_s^+ / \beta_s) [(1 + P_F) / P_F], \text{ but } l > 3l_D. \quad (32)$$

The larger l is, the less P_{Jl} is, and the greater the threshold current J_t (Equation (15)) is.

[0076] The conditions of the 100% spin polarization electrons are $J_S^+ \gg J_0 \gg J_S$. Taking into account Equations (10), (28) and (31) these conditions can be written as:

$$\frac{n_S}{n_S^+} \left(\frac{D}{L_S v_{\mu}} \right) < T_t(\mu_s^+) \ll \left(\frac{D^+}{L_S^+ v_{\mu}} \right) \quad (33)$$

$$\frac{n_S^+}{n_S} \gg \left(\frac{D L_S^+}{D^+ L_S} \right) = \left(\frac{D \tau_S^+}{D^+ \tau_S} \right)^{1/2}, \quad (34)$$

wherein the parameter $T_t(\mu_s^+)$ is equal to

$$T_t(\mu_s^+) = d_0 \exp\left[-\frac{\eta l_D (\Delta - \mu_s^+)^{1/2}}{l_0 \Delta^{1/2}}\right]. \quad (35)$$

[0077] The conditions of Equations (19) and (33)-(35) can be rewritten as:

$$\frac{2l_0}{\eta} \ln\left(\frac{L_S^+ v_{\mu} d}{D^+}\right) < l_D < \frac{l_0}{\eta} \ln\left(\frac{n_S^+ L_S v_{\mu} d}{n_S D}\right) \quad (36)$$

$$3l_D < l \approx L_S^+ \quad (37)$$

where l_D is given by Equation (20), that is, l_D is determined by $n_S^+ = N_d^+$.

[0078] The condition of Equations (33)-(36) determine the requirements of the electron densities $n_S = N_d$ and $n_S^+ = N_d^+$ in the n+-S' layer 320 and the n-semiconductor 310, the thickness l_D of the Schottky depletion layer of FM-n+-S' junction, the thickness l of the n+-S' layer and also the value of a jump Δ_0 of the bottom of the conduction band, $E_c(x)$, at the boundary of the n±S' layer 320 and the semiconductor 310, $\Delta_0 = (E_{c0} - E_{c0}^+)$ both for the case of a degenerate semicon-

ductor **310** as shown in **FIG. 3B** and for the case of a nondegenerate semiconductor **310** as shown in **FIG. 3C**. Here E_{c0} and E_{c0}^+ are the bottom of the conduction band in the semiconductor **310** and the thin degenerate semiconductor n^+ -layer **320** near the semiconductor **310**. Typical parameters for Ni—GaAs—GaAlAs heterostructure are $N_d^+ \approx 10^{18} \text{-cm}^{-3}$ — 10^{19}-cm^{-3} , $N_d^+ \approx 10^{16} \text{-cm}^{-3}$ — 10^{17}-cm^{-3} , $\Delta \approx 500 \text{ meV}$, $\mu_s^+ \approx 30 \text{ meV}$ — 50 meV , $l_D \approx 5 \text{ nm}$, $l_p \approx 5 \text{ nm}$, $l \approx 1 \text{ nm}$, $L_s \approx 300 \text{ nm}$, and $\Delta_0 \approx \mu_s^+$.

[0079] **FIG. 4A** illustrates a spin polarizer **400** according to another embodiment of the present invention. As shown, the spin polarizer **400** may include a nonmagnetic semiconductor **410**, a ferromagnetic **430** and two thin negative and positive highly doped degenerate nonmagnetic semiconductor layers, n^+ -S layer **420** and p^+ -S layer **415**. The layers **420** and **415** are situated between the ferromagnet **430** and negative doped semiconductor, n-S region **410**, and have the same energy bandgap as that of the n-S region **410**. These layers can be formed by heavily negative and positive doping of a portion of the semiconductor **410**. The spin polarizer **400** may also include electrodes **440** and **450** electrically connecting the ferromagnetic layer **430** and the semiconductor **410**, respectively. The spin polarizer **400** thus formed may be described as having a FM- n^+ - p^+ -n-S heterostructure with parameters adjusted to sufficiently meet the conditions described below.

[0080] The ferromagnetic layer **430** may be formed from various magnetic materials, preferably Ni, Fe and Co, as well as various magnetic alloys, which may include one or a combination of Fe, Co, Ni. The semiconductor **410** may be formed from various nonmagnetic semiconductor materials including Si, GaAs, ZnTe, GaSb, GaP, Ge, InAs, CdSe, InP, InSb, CdTe, CdS, ZnS, ZnSe, AlP, AlAs, AlSb and also alloys of these materials. In general, it is preferred that the semiconductor **410** be formed from semiconductor materials with relatively large electron spin relaxation time, L_s , for example GaAlAs, InAs, ZnSe and ZnCdSe. The semiconductor **410** can be negatively doped.

[0081] **FIG. 4B** illustrates an exemplary energy diagram of the spin polarizer shown in **FIG. 4A** along the line IV-IV in the case when the n-S region is a degenerate nonmagnetic semiconductor. The device **400** may be used at arbitrary temperature and may ensure spin polarization of electrons close to 100% in an area of the nonmagnetic semiconductor near the boundary with the p^+ -S layer **415**. F is the Fermi level; Δ and l_D are the height and thickness of the Schottky barrier of the FM- n^+ -S junction, l_p is the total thickness of the highly doped p^+ -S layer **415**, l is the total thickness of the highly doped n^+ -S layer **420** and the p^+ -S layer **415**, $E_c(x)$ is the bottom of the conduction band and $E_v(x)$ is the top of the valence band of the semiconductor **420**, **415** and **410**, E_{c0} and E_{c0}^+ are the bottoms of the conduction band in the n-S region **410** and the part of the n^+ -S layer **420** corresponding to a region of the degenerate semiconductor, where $l_D < x < (l - l_p)$.

[0082] **FIG. 4C** illustrates an exemplary energy diagram of the spin polarizer shown in **FIG. 4A** along the line IV-IV in the case when the n-S region is a nondegenerate nonmagnetic semiconductor. The device **300** can be used at relatively high temperatures, including room temperatures, and can ensure spin polarization of electrons close to 100% in an area of the nonmagnetic semiconductor near the

boundary with the p^+ -S layer **415**. F is the Fermi level; Δ and l_D are the height and thickness of the Schottky barrier of the FM- n^+ -S junction, l_p is the total thickness of the highly doped p^+ -S layer **415**, l is the total thickness of the highly doped n^+ -S layer **420** and the p^+ -S layer **415**, $E_c(x)$ is a bottom of the conduction band and $E_v(x)$ is a top of the valence band of the semiconductors **420**, **415** and **410**, E_{c0} and E_{c0}^+ are the bottoms of the conduction band in the n-S region **410** and the part of the n^+ -S layer **420** corresponding to the degenerate semiconductor corresponding to $l_D < x < (l - l_p)$.

[0083] A spin polarization close to 100% in the FM- n^+ - p^+ -n-S heterostructure shown in **FIG. 4** can be achieved if the following condition is satisfied:

$$l_D < l_p < j N L_s^+ \quad (38)$$

and

$$N_d l_p^2 \approx 2 \epsilon \epsilon_0 (E_{c0} - E_{c0}^+) / q^2 \quad (39)$$

wherein E_{c0} and E_{c0}^+ are the bottoms of the conduction band in the n-S region **410** and the part of the n^+ -S layer **420** corresponding to the degenerate semiconductor, where $l_D < x < (l - l_p)$, in **FIGS. 4B and 4C**, respectively. For example, equations (38) and (39) may be satisfied if N_d ranges between 10^{18} cm^{-3} and 10^{19} cm^{-3} , $l_p \leq 30 \text{ nm}$, and E_{c0} and $(E_{c0} - E_{c0}^+) \approx 50 \text{ meV}$. When the conditions of Equations (38) and (39) are satisfied the region of the n^+ -S layer **420** and the p^+ -S layer **415** corresponding to $l_D < x < l$ is a degenerate semiconductor and the energy diagram of the FM- n^+ - p^+ -n-S heterostructure, spin polarizer, has the form shown in **FIG. 4B or 4C** for the case when the n-S region **410** is a degenerate semiconductor or nondegenerate semiconductor, respectively. One can see that these energy diagrams coincide qualitatively with those shown in **FIGS. 3B and 3C**. Therefore, all results presented above for the FM- n^+ -S heterostructure shown in **FIGS. 4A-4C** are valid for the FM- n^+ - p^+ -n-S heterostructure, spin polarizer, shown in **FIGS. 4A-4C**.

[0084] All of the above described structures and conditions are also valid for a negatively doped semiconductor. In this case the words electrons, donor and acceptor should be substituted for the words holes, acceptor and donor, respectively, and the n-, n^+ - and p-semiconductor regions should be substituted for p-, p^+ - and n-semiconductor regions.

[0085] Different spintronic devices based on ferromagnetic-semiconductor-ferromagnetic (FM-S-FM) structures have been suggested, including those using an electric field [5,6], external magnetic field [7], and a nanowire current [8] to control an electron spin. All of these devices are spin valves where one of FM-S junctions works as a spin injector and another one as a spin filter. The spin filter efficiently admits electrons with a certain spin projection and efficiently reflects electrons with the opposite spin. The spin polarizer and the FM- n^+ -n-S heterostructures shown in the **FIGS. 3 and 4** according to an embodiment of the present invention can be used as the spin filter and spin injector in all of these devices and also in semiconductor systems for quantum computing.

[0086] One of possible variant of use of the spin polarizer and the FM- n^+ -n-S heterostructures is shown in **FIG. 5**. **FIG. 5A** illustrates an exemplary variant of a spin device according to an embodiment of the present invention based on a ferromagnetic-semiconductor FM- n^+ -n- n^+ -FM hetero-

structure containing a donor doped nonmagnetic semiconductor (n-S) layer localized between two ferromagnetic metal (FM) layers and also two thin layers of a heavily doped degenerate semiconductor (n⁺-S layers) between the n-S and FM layers. FIG. 5B illustrates an exemplary energy diagram of the spin device shown in FIG. 5A in equilibrium (broken curves) and at a bias voltage V (solid curves) in the case when the n⁺-S layers have a narrower energy bandgap than that of the n-S region and the n-S region is a degenerate semiconductor (for the nondegenerate n-S region $E_{c0} > F$). Here F is the Fermi level in equilibrium, $E_C(x)$ is bottom of semiconductor conduction band, E_{c0} and $E_{c0}^+ -$ are the values of $E_C(x)$ in the n-S and n⁺-S layers, respectively; w and l thickness of the n-S and n⁺-S layers, respectively; Δ and l_D^- are the height and thickness of the Schottky barrier of the FM-n⁺-S junctions. One of the two FM-n⁺-S contacts is used as a spin injector and another as a spin filter. Such FM-n⁺-n⁺-S heterostructures ensure spin polarization of electrons is equal substantially to 100% inside the nonmagnetic semiconductor layer when its thickness, w, is less than L_s and magnetizations, M_1 and M_2 have opposite direction, as it is shown in the FIG. 5A. This occurs even in these cases when spin polarization of the current in FM-n⁺-S contacts, P_j , are relatively small (P_j can be even ~5%-15%).

[0087] The present invention has been described with reference to an exemplary embodiment. However, it will be readily apparent to those skilled in the art that it is possible to embody the invention in specific forms other than that of the exemplary embodiment described above. This may be done without departing from the spirit and scope of the invention. The exemplary embodiment is merely illustrative and should not be considered restrictive in any way. The scope of the invention is given by the appended claims, rather than the preceding description, and all variations and equivalents that fall within the range of the claims are intended to be embraced therein.

What is claimed is:

1. A spintronics device spin polarizer, comprising:

a nonmagnetic semiconductor;

a ferromagnetic layer formed above the semiconductor layer; and

a thin degenerate semiconductor layer formed between the ferromagnetic layer and the semiconductor which is more highly doped than the semiconductor layer;

wherein the concentration of shallow donors N_d^+ in the thin degenerate semiconductor layer satisfies the condition $4\pi N_d^+ a_B^3/3 > 1$, where a_B is the Borh radius of the shallow donor; and wherein the thickness, l, of the thin degenerate semiconductor layer satisfies the condition: $6l_0 \leq 2l_d \leq 1 < L_{S^+}$ wherein:

l_0 represents a tunneling length for a Schottky barrier near a junction between the ferromagnetic layer and the thin degenerate semiconductor layer given by the equation $l_0 = (\hbar^2/8\pi^2 m^* \Delta)^{1/2}$;

l_D represents a thickness of the Schottky barrier given by $l_D = (2\epsilon\epsilon_0 \Delta/q^2 N_d^+)^{1/2}$; and

L_{S^+} represents a length of electron spin in the thin degenerate semiconductor layer given by the equation $L_{S^+} = \sqrt{D^+ \tau_s^+}$ where D^+ and τ_s^+ are diffusion coefficient

and time of spin coherence of electrons in the thin degenerate semiconductor n⁺-S layer, respectively.

2. The device of claim 1, wherein the ferromagnetic layer is formed from magnetic semiconductors.

3. The device of claim 1, wherein the ferromagnetic layer is formed from magnetic metals.

4. The device of claim 1, wherein the ferromagnetic layer is formed from any one of Ni, Fe and Co, or an alloy thereof.

5. The device of claim 1, wherein the semiconductor is formed from at least one semiconductor material selected from the list consisting of: Si, GaAs, ZnTe, GaSb, GaP, Ge, InAs, CdSe, InP, InSb, CdTe, CdS, ZnS, ZnSe, AlP, AlAs, AlSb, CuI, CuCl, CuBr.

6. The device of claim 1, wherein the semiconductor is formed from an alloy of any of Si, GaAs, ZnTe, GaSb, GaP, Ge, InAs, CdSe, InP, InSb, CdTe, CdS, ZnS, ZnSe, AlP, AlAs, AlSb, CuI, CuCl, CuBr.

7. The device of claim 1, wherein the semiconductor has a large electron spin relaxation length, L_s .

8. The device of claim 1, wherein:

the thickness l of the thin degenerate semiconductor layer is greater than 1 nanometer and less than or equal to 500 nanometers.

9. The device of claim 1, wherein the semiconductor and the thin degenerate semiconductor layer may be negatively doped such that a donor concentration N_d^+ in the thin degenerate semiconductor layer is greater than a donor concentration N_d in the semiconductor.

10. The device of claim 9, wherein the thin degenerate semiconductor layer is doped with n-dopant metals selected from the group consisting of P, As, and Sb when the semiconductor is Si or Ge, and Ge, Se, Te, Si, Pb and Sn when the semiconductor is GaAs.

11. The device of claim 1, wherein the semiconductor and the thin degenerate semiconductor layer are positively doped such that an acceptor concentration N_a^+ in the thin degenerate semiconductor layer is greater than an acceptor concentration N_a in the semiconductor.

12. The device of claim 1, wherein an energy band gap of the thin degenerate semiconductor layer is narrower than an energy band gap of the semiconductor by a value

$\Delta_0 = (E_{c0} - E_{c0}^+)$ for a negatively doped semiconductor layer, wherein E_{c0} and E_{c0}^+ are bottoms of the conduction band in the semiconductor and the thin degenerate semiconductor layer near the semiconductor, and

$\Delta_0 = (E_{v0} - E_{v0}^+)$ for a positively doped semiconductor layer, wherein E_{v0} and E_{v0}^+ are tops of the valence band in the semiconductor and the thin degenerate semiconductor layer near the semiconductor.

13. The device of claim 1, wherein:

a donor concentration of N_d^+ of the thin degenerate semiconductor layer substantially ranges from 10^{18} cm^{-3} to 10^{20} cm^{-3} for a negatively doped semiconductor layer, and

an acceptor concentration of N_a^+ of the thin degenerate semiconductor layer substantially ranges from 10^{18} cm^{-3} to 10^{20} cm^{-3} for a positively doped semiconductor layer.

14. The device of claim 1, wherein the thin degenerate semiconductor layer is formed from at least one of various semiconductor materials selected from the group consisting

of: Si Ge, GaAlAs, ZnTe, GaSb, GaAlP, InAsP, CdSeTe, InSbP, ZnCdS, AlAsP, AlAsSb, AlSb, CuClI, CuClBr, and alloys thereof.

15. The device of claim 1, wherein the thin degenerate semiconductor layer of thickness l satisfies the condition: $6l_0 \leq 2l_d \leq l \ll L_S^+$; and wherein the thin degenerate semiconductor layer is formed from the same negatively doped semiconductor material as the semiconductor layer.

16. The device of claim 15, wherein the thin degenerate semiconductor layer comprises two layers.

17. The device of claim 16, wherein one of said two layers is heavily donor doped, and the other of the two layers is heavily acceptor doped.

18. The device of claim 17, wherein the donor heavily doped layer closes the ferromagnetic layer and acceptor heavily doped layer closes the semiconductor.

19. The device of claim 18, wherein the acceptor concentration, N_a , and thickness of the second acceptor doped layer, l_p , satisfies conditions:

$$l_D < l_p < l \ll L_S^+ \text{ and } N_a l_p^2 \approx 2\epsilon\epsilon_0(E_{c0} - E_{v0}^+)/q^2$$

wherein:

ϵ represents a permittivity of the semiconductor;

ϵ_0 represents a permittivity vacuum;

l_D represents a thickness of the Schottky barrier given by $l_D = (2\epsilon\epsilon_0\Delta/q^2 N_d^+)^{1/2}$;

L_S^+ represents a length of electron spin in the thin degenerate semiconductor layer given by the equation $L_S^+ = \sqrt{D^+ \tau_s^+}$ where D^+ and τ_s^+ are diffusion coefficient and time of spin coherence of electrons in the thin degenerate semiconductor n^+ -S layer, respectively;

$q > 0$ represents is the elementary charge, and

E_{c0} and E_{v0}^+ are bottoms of the conduction band in the semiconductor and the thin degenerate semiconductor layer near the semiconductor.

20. The device of claim 18, wherein the donor concentration, N_d , and thickness of the second donor doped layer, l_d , satisfy conditions:

$$l_D < l_d < l \ll L_S^+ \text{ and } N_d l_d^2 \approx 2\epsilon\epsilon_0(E_{v0} - E_{v0}^+)/q^2$$

wherein:

ϵ represents a permittivity of the semiconductor;

ϵ_0 represents a permittivity vacuum;

l_D represents a thickness of the Schottky barrier given by $l_D = (2\epsilon\epsilon_0/q^2 N_d^+)^{1/2}$;

L_S^+ represents a length of electron spin in the thin degenerate semiconductor layer;

$q > 0$ represents is the elementary charge, and

E_{v0} and E_{v0}^+ are tops of the valence band in the semiconductor and the thin degenerate semiconductor layer near the semiconductor.

21. The device of claim 1 wherein a positive bias voltage is applied to the ferromagnetic layer and a negative bias voltage is applied to the semiconductor layer.

22. The device of claim 1, wherein:

a nonmagnetic semiconductor layer is localized between two ferromagnetic layers;

two thin degenerate semiconductor layers formed between the two ferromagnetic layers and the semiconductor layer which are more highly doped than the semiconductor layer; wherein the concentration of shallow donors N_d^+ in the thin degenerate semiconductor layers satisfies the condition $4\pi N_d^+ a_B^3 / 3 > 1$, where a_B is the Borh radius of the shallow donor; and wherein the thickness, l , of the thin degenerate semiconductor layers satisfies the condition: $6l_0 \leq 2l_d \leq l \ll L_S^+$ wherein:

l_0 represents a tunneling length for a Schottky barrier between the ferromagnetic layers and the thin degenerate semiconductor layer given by the equation $l_0 = (\hbar^2 / 8\pi^2 m^* \Delta)^{1/2}$;

L_S^+ represents a length of electron spin in the thin degenerate semiconductor layer given by the equation $L_S^+ = \sqrt{D^+ \tau_s^+}$ where D^+ and τ_s^+ are diffusion coefficient and time of spin coherence of electrons in the thin degenerate semiconductor n^+ -S layer, respectively;

l_D represents a thickness of the Schottky barrier given by $l_D = (2\epsilon\epsilon_0\Delta/q^2 N_d^+)^{1/2}$ wherein ϵ_0 is the permittivity of free space, ϵ is the relative permittivity of the thin degenerate semiconductor layer, $q > 0$ is the elementary charge, and N_d^+ is the concentration of shallow donors in the thin degenerate semiconductor layers, Δ is a height of the Schottky barrier; and the thickness of the nonmagnetic semiconductor n -S layer w , is less than L_S , where L_S represents a length of electron spin in the semiconductor layer given by the equation $L_S = \sqrt{D \tau_s}$ where D and τ_s are diffusion coefficient and time of spin coherence of electrons in the semiconductor n -S layer, respectively; and magnetizations, M_1 and M_2 , in the ferromagnetic layers are directed in opposite directions.

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